

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	10	(merck-martin\$).in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 16:14
S1	16	operand\$1 near4 stack\$3 near4 length\$1	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:01
S2	0	(operand\$1 near4 stack\$3) with (length\$1 near4 memor\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:17
S3	1	(operand\$1 near4 stack\$3) same (length\$1 near4 memor\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:17
S4	5	(operand\$1 near4 stack\$3) same (length\$1 near4 table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:19
S5	5	(operand\$1 near4 stack\$3) same ((length\$1 or size\$1) near4 table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:19
S6	5	(operand\$1 near4 stack\$3) same ((length\$1 or size\$1) with table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:41
S7	9	(operand\$1 with stack\$3) same ((length\$1 or size\$1) with table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:01
S8	149	(712/202).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:38
S9	129	(712/25).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:38
S10	424	(712/200).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:38

## EAST Search History

S11	469	(712/210).CCLS.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:38
S12	75	(data near4 stack\$3) same ((length\$1 or size\$1) with table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:02
S13	2083	(stack\$3) same ((length\$1 or size\$1) with table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:03
S14	383	(stack\$3) with ((length\$1 or size\$1) near4 table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:58
S15	216	(stack\$3) near4 ((length\$1 or size\$1) near4 table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:58
S16	80	(stack\$3) near4 ((length\$1) near4 table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:00
S17	39	(stack\$1) near4 ((length\$1) near4 table\$1)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 14:58
S18	0	((stack\$3) near4 ((length\$1) near4 table\$1)) and (("7"?).ccls.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:00
S19	1	(operand\$1 near4 stack\$3 near4 length\$1).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:01
S20	0	((operand\$1 same stack\$3) same ((length\$1 or size\$1) same table\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:02
S21	149	((operand\$1 same stack\$3) same ((length\$1 or size\$1) same table\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:02
S22	5	((data near4 stack\$3) same ((length\$1 or size\$1) with table\$1)). clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:02

## EAST Search History

S23	7	((data with stack\$3) same ((length\$1 or size\$1) with table\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:03
S24	155	((data with stack\$3) same ((length\$1 or size\$1) with table\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:03
S25	177	((stack\$3) same ((length\$1 or size\$1) with table\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:03
S26	10	((stack\$3) same ((length\$1 or size\$1) with table\$1) same (type\$1)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:04
S27	314	((stack\$3) same ((length\$1 or size\$1) with table\$1) same (type\$1))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:05
S28	3	((stack\$3) same ((length\$1 or size\$1) with table\$1) same (type\$1 near4 memori\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:05
S29	12627	((stack\$3) and ((length\$1 or size\$1) and table\$1) and (type\$1 and memori\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:06
S30	1486	((stack\$3) and ((length\$1 or size\$1) same table\$1) and (type\$1 same memori\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:06
S31	0	((stack\$3) and ((length\$1 or size\$1) same table\$1) and (type\$1 same memori\$3)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/07/05 15:06

**Scholar** All articles - [Recent articles](#) Results 1 - 10 of about 8,970 for **stack + operand + type + length + table**. (0.17 seconds)

All Results

[C Fraser](#)

[R Weicker](#)

[S Colley](#)

[J Gosling](#)

[A Adl-Tabataba...](#)

[Java intermediate bytecodes: ACM SIGPLAN workshop on intermediate representations \(IR'95\)](#)

J Gosling - ACM SIGPLAN Notices, 1995 - portal.acm.org

... The simplest is **operand stack** overflow and underflow: the **length** of the **stack** portion of the **type** state is the depth that the **operand stack** will have ...

Cited by 131 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Implications of structured programming for machine architecture - all 2 versions »](#)

AS Tanenbaum - Communications of the ACM, 1978 - portal.acm.org

... if **operand** < 0 4 1 branch if **operand** > 0 4 1 ... Since measurements of the **type** presented in this paper are ... the need for memory cycles when referencing the **stack**. ...

Cited by 66 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[Compilers using a universal intermediate language](#)

US Patent 4,667,290, 1987 - freepatentsonline.com

... 1 is set if the second **operand** is a **stack operand**. ... use bits for the 3 operands (top for first **operand**). ... the size of the object or **type**; alignment information ...

Cited by 148 - [Related Articles](#) - [Cached](#) - [Web Search](#)

[Data processing system - all 2 versions »](#)

SR Colley, GW Cox, JR Rattner, RC Swanson - US Patent 4,325,120, 1982 - Google Patents

... **STACK SEG. LENGTH** ^IO3 **CONTROL SEG. LENGTH** ^>97 **CONTEXT AL.LENGTH** - -f - 1 **INSTRUCTION SEG.** - 90 ... » **BASE TYPE** - DATA AB 64 Z0 **OPERAND STACK SEG.** -IO6 ...

Cited by 137 - [Related Articles](#) - [Web Search](#)

[Register Allocation for Free: The C Machine Stack Cache](#)

DR Ditzel, HR McLellan - portal.acm.org

... Executed 391,830,910 **Table 4. Operand Usage for Registerless Machine (VAX) Type Count %Memory** ... Memory references per instruction with **Stack Cache** registers ...

Cited by 61 - [Related Articles](#) - [Web Search](#)

[A case study of VAX-11 instruction set usage for compiler execution](#)

CA Wiecek - ACM SIGARCH Computer Architecture News, 1982 - portal.acm.org

... **type** (byte, word, longword, quadword, and ... onto or pop registers from the **stack** are analyzed ... in this group provide information about floating point **operand** values ...

Cited by 40 - [Related Articles](#) - [Web Search](#)

[Active sensor networks - all 14 versions »](#)

P Levis, D Gay, D Culler - Proceedings of the second international conference on ..., 2005 - usenix.org

... languages must provide serialization support for their data **types**. ... Operation, Width, Name, **Operand Bits**, Description. ... pushc6, 1, pushc, 6, Push a constant on **stack** ...

Cited by 44 - [Related Articles](#) - [Cached](#) - [Web Search](#)

[Supporting FPGA microprocessors through retargetable software tools - all 5 versions »](#)

DA Clark, BL Hutchings - FPGAs for Custom Computing Machines, 1996. Proceedings. IEEE ..., 1996 - ieeexplore.ieee.org

... hard-ware can support the following **types** of instructions ... limits instructions to no more than one **operand**. ... a set of base instructions, **stack** protocol, register ...

Cited by 34 - [Related Articles](#) - [Web Search](#) - [Library Search](#)

[Java bytecode to native code translation: the caffeine prototype and preliminary results - all 4 versions »](#)

CHA Hsieh, JC Gyllenhaal, WH Wen-mei - Proceedings of the 29th annual ACM/IEEE international ..., 1996 - portal.acm.org

... register mapping, a push to the **operand stack** is translated ... translation falls back to the **stack** computation model. ... Since the class run-time **type** information in ...

Cited by 86 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Dhrystone: a synthetic systems programming benchmark - all 4 versions »](#)

RP Weicker - Communications of the ACM, 1984 - portal.acm.org

... Dynamic data were collected only for Pascal **stack** machine instructions ... **Operand Types** Another breakdown can be made according to the data **type** of the ...

Cited by 176 - [Related Articles](#) - [Web Search](#)

**Scholar** All articles - [Recent articles](#) Results 1 - 10 of about 6,670 for **stack + operand + length + table**. (0.15 seconds)

**All Results**

[A Tanenbaum](#)

[A Adl-Tabataba...](#)

[J Ernst](#)

[S Colley](#)

[K King](#)

[A code compression system based on pipelined interpreters - all 7 versions »](#)

J Hoogerbrugge, L Augusteijn, J Trum, R van de ... - Software Practice and Experience, 1999 - doi.wiley.com

... nearly 256 entries, the jump **table** corresponding to ... of superscalar implementations of variable **length** instruction set ... the first two **stack operands** are directly ...

[Cited by 50](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Fast, effective code generation in a just-in-time Java compiler - all 23 versions »](#)

AR Adl-Tabatabai, M Cierniak, GY Lueh, VM Parikh, ... - Proceedings of the ACM SIGPLAN 1998 conference on ..., 1998 - portal.acm.org

... such as floating-point con- stants and switch **tables**. ... For each Java **operand stack** location and variable that contains ... when an array of constant size is created ...

[Cited by 142](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[The case against \*\*stack\*\*-oriented instruction sets](#)

GJ Myers - ACM SIGARCH Computer Architecture News, 1977 - portal.acm.org

... **length**, and storage addresses are 20 D its in **length**. ... fewer address bits are needed if an **operand** is in a ... no address bits are needed in **stack** instructions that ...

[Cited by 13](#) - [Related Articles](#) - [Web Search](#)

[Stack mechanism with the ability to dynamically alter the size of a \*\*stack\*\* in a data processing ... - all 2 versions »](#)

PE Stanley, P Szorc - US Patent 4,524,416, 1985 - Google Patents

... has a maximum number of allocatable storage locations with the actual physical size of the **stack** being equal to the total number of **operands** stored therein. ...

[Cited by 30](#) - [Related Articles](#) - [Web Search](#)

[A Fortran Language System for Mutation-based Software Testing - all 9 versions »](#)

KN King, AJ Offutt - Software - Practice and Experience, 1991 - cs.ubc.ca

... a relational operator replacement) and the new **operand** to be substituted into the code **table** at that ... Because of its size, the Mothra tools access this file ...

[Cited by 113](#) - [Related Articles](#) - [View as HTML](#) - [Web Search](#)

[A practical tool kit for making portable compilers - all 4 versions »](#)

AS Tanenbaum, H van Staveren, EG Keizer, JW ... - Communications of the ACM, 1983 - portal.acm.org

... unsigned, real, pointer, or set) and an **operand length**, which may ... the placement of variables within the **stack** frame. ... we will assume a 16-bit word size and 16 ...

[Cited by 55](#) - [Related Articles](#) - [Web Search](#)

[Code compression - all 6 versions »](#)

J Ernst, W Evans, CW Fraser, TA Proebsting, S ... - Proceedings of the ACM SIGPLAN 1997 conference on ..., 1997 - portal.acm.org

... It suffices to note that the code is **stack**-based, that ... sp and 24, can be compacted into a single **operand** byte ... However, the pro- gram size reduction P is this 1 ...

[Cited by 135](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Digital computer system incorporating object-based addressing and access control and tables defining ... - all 3 versions »](#)

RH Gruner, GF Clancy, CJ Mundie, SI Schleimer, SJ ... - US Patent 4,731,734, 1988 - Google Patents

... internal mech- anisms, for example, **stack** mechanisms and ... is to derive the logical address of the **operand** ... means further derives the **length** specifier specified ...

[Cited by 21](#) - [Related Articles](#) - [Web Search](#)

[A case study of VAX-11 instruction set usage for compiler execution](#)

CA Wiecek - ACM SIGARCH Computer Architecture News, 1982 - portal.acm.org

... onto or pop registers from the **stack** are analyzed ... on arbitrary byte boundaries and have variable **length**. ... are followed by zero through six **operand** specifiers as ...

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[Use of \*\*stack\*\* depth to identify machine code mistakes - all 2 versions »](#)

**Scholar** All articles - Recent articles Results 1 - 10 of about 8,070 for **stack + operand + length**. (0.12 seconds)

All Results

[M Dahm](#)

[A Adl-Tabataba...](#)

[J Gosling](#)

[S McCanne](#)

[M Cierniak](#)

**Stack** mechanism with the ability to dynamically alter the size of a **stack** in a data processing ... - all 2 versions »

PE Stanley, P Szorc - US Patent 4,524,416, 1985 - Google Patents

... has a maximum number of allocatable storage locations with the actual physical size of the **stack** being equal to the total number of **operands** stored therein. ...

[Cited by 30](#) - [Related Articles](#) - [Web Search](#)

Fast, effective code generation in a just-in-time Java compiler - all 23 versions »

AR Adl-Tabatabai, M Cierniak, GY Lueh, VM Parikh, ... - Proceedings of the ACM SIGPLAN 1998 conference on ..., 1998 - portal.acm.org

... For each Java **operand stack** location and variable that contains a reference to an array A, the ... for a constant index or when an array of constant size is created ...

[Cited by 142](#) - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

... processor unit for executing instructions with a special **operand** specifier of indeterminate **length** - all 2 versions »

WD Strecker, TN Hastings, RF Lary, DP Rodgers, SH ... - US Patent 4,241,397, 1980 - Google Patents

... [54] CENTRAL PROCESSOR UNIT FOR EXECUTING INSTRUCTIONS WITH A SPECIAL **OPERAND SPECIFIER**

OF INDETERMINATE **LENGTH** [75] Inventors: William D. Strecker, Harvard; ...

[Cited by 39](#) - [Related Articles](#) - [Web Search](#)

Use of **stack** depth to identify machine code mistakes - all 2 versions »

RL Sites - US Patent 5,450,575, 1995 - Google Patents

... speeds. An- ever, the total **length** of the instruction is not yet known ... system cumulative offset, along with any **stack-based operand** (eg ...

[Cited by 51](#) - [Related Articles](#) - [Web Search](#)

The case against **stack**-oriented instruction sets

GJ Myers - ACM SIGARCH Computer Architecture News, 1977 - portal.acm.org

... However register and 8 Page 3. **stack** forms also have the following disadvantages:

I. They are oriented toward fixed-size **operands**. If ...

[Cited by 13](#) - [Related Articles](#) - [Web Search](#)

The **stack** growth function: cache line reference models - all 8 versions »

M Kobayashi, MH MacDougall - IEEE Transactions on Computers, 1989 - doi.ieeecomputersociety.org

... 2. Instruction and **operand** mean working set size. ... CONCLUDING REMARKS The accuracy with which these **stack**-based functions and models represent cache line ...

[Cited by 22](#) - [Related Articles](#) - [Web Search](#)

A practical tool kit for making portable compilers - all 4 versions »

AS Tanenbaum, H van Staveren, EG Keizer, JW ... - Communications of the ACM, 1983 - portal.acm.org

... instructions have a type (integer, unsigned, real, pointer, or set) and an **operand length**, which may either be explicit or pepped from the **stack** at run-time. ...

[Cited by 55](#) - [Related Articles](#) - [Web Search](#)

Use of **stack** depth to identify architecture and calling standard dependencies in machine code - all 3 versions »

TR Benson - US Patent 5,301,325, 1994 - Google Patents

... by Richard L. Sites, for "USE OF **STACK DEPTH** TO ... to six **operand** specifiers, where each **operand** specifier is from one byte to many bytes in **length**. ...

[Cited by 51](#) - [Related Articles](#) - [Web Search](#)

A case study of VAX-11 instruction set usage for compiler execution  
CA Wiecek - ACM SIGARCH Computer Architecture News, 1982 - portal.acm.org  
... in the instruction stream, variable **length** bit field ... onto or pop registers from the  
**stack** are analyzed ... and register reads by instruction **operand** specifiers when ...  
Cited by 40 - Related Articles - Web Search

A code compression system based on pipelined interpreters - all 7 versions »  
J Hoogerbrugge, L Augusteijn, J Trum, R van de ... - Software Practice and Experience, 1999 -  
doi.wiley.com  
... in the decoding of superscalar implementations of variable **length** instruction set ...  
Therefore, the first two **stack operands** are directly available in registers ...  
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Result Page: 1 2 3 4 5 6 7 8 9 10 **Next**

stack + operand + length

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